



Arnaud TISSERAND

CNRS Senior Researcher
Computer Arithmetic

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*Investigating the links
between representations
of numbers, computing algorithms,
performance, energy consumption
and security of hardware/software
implementations*

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BIO

After obtaining his PhD in Computer Science at the École Normale Supérieure in Lyon in 1997, Arnaud Tisserand spent two years in the Swiss Center for Microelectronics and Microelectronic (CSEM) in Neuchâtel, before joining INRIA and the LIP laboratory Lyon as researcher in 1999. In 2005, he moved to a CNRS researcher position in the LIRMM Montpellier, then IRISA Lannion in 2008, and finally in 2016 the Lab-STICC Lorient, a common research unit CNRS/UBS. His main research activities are in hardware/software arithmetic with links in hardware architectures and associated tools. In cybersecurity, he studies cryptographic implementations, hardware architectures and protections against physical attacks.



[Link to full biography](#)



50% of the researcher's activity devoted to cybersecurity

Focus :

Research

Application field

Core data

PhD students: 24

Publications: 25 - IEEE TC, IEEE ESL, ACM TECS, ACM TOMS, etc.

Conferences: 81 - ARITH, CHES, ASYNC, IndoCrypt, ECC, SECURE, WAIFI, etc.

Award(s): 2

Patent(s): 1

International collaborations: University College Cork (Ireland).

Area(s) of research

Arithmetic implementations
Low-power implementations
Cryptographic Implementations
Hardware Security
Protections against attacks
Embedded computing libraries
Design tools for hardware architectures

Applicative examples

Embedded Systems
Computing Devices

Responsibilities

Head of the ARCAD research team

Domain

Computer Arithmetic

Keywords

Computation Algorithms
Number Systems
Low-power
Security (attacks)
Reliability (faults)
Sharp computing accuracy

Contact

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