



Guy GOGNIAT

Full Professor
Embedded systems

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*Collaborative research is a good
to be cultivated with passion*

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BIO

After obtaining his PhD at Nice-Sophia Antipolis University in 1997, Guy Gogniat joined Université Bretagne Sud (University of South Brittany) in 1998. In 2004-2005, he was invited to the University of Massachusetts, Amherst, USA where he worked on the security of embedded systems using reconfigurable technologies. While his research activities cover many fields (model-based design methodologies, adaptive systems, reconfigurable architectures, hardware and software design), since 2003 he is more specifically interested in the security of embedded systems and future applications around health and Industry 4.0. His research benefits from national and European funding programs.



Link to full biography

Core data

PhD students: 25

Post-doctoral fellows: 4

Publications: 40 - IEEE TVLSI, IEEE TC, ACM TODAES, ACM TECS, etc.

Conferences: 130 - DATE, FPL, FCCM, FPGA, FPT, ReCoSoC, DSD, etc.

Book(s): Security Trends for FPGAs - Springer.

Award(s): Best Paper Award (ReCoSoC 2015).

Patent(s): Reconfiguration method for an electronic circuit set of components (2008).

International collaborations: Ruhr-Universität Bochum (Allemagne), University of Massachusetts (USA), Université Libre de Bruxelles (Belgique), Université Technique de Munich (Allemagne), Information Technology University (Pakistan), Politecnico di Milano (Italie), etc.



100% of the researcher's activity devoted to cybersecurity

Focus :

Research

Application field

Area(s) of research

Embedded systems security

Fields of expertise

Protection of embedded system architectures.
Protection of manycore architectures.
Development of crypto-processors.
Code Obfuscation.

Applicative examples

Protection of health data and multimedia applications.
Operating systems protection.

Responsibilities

- Vice-President Research UBS (2016-2020)
- Assessment Committee NRA Global Security and Cybersecurity (2017-2020)
- Lab-STICC Deputy Director (2010-2016)
- Co-Leader of the Math-STIC disciplinary group of the SICMA doctoral school (2010-2016)
- Institute of Information Sciences and their Interactions Scientific Council CNRS INS2I (2015-2018)
- Co-Leader theme C research group ISIS (2009-2015)
- Co-responsible for the digital security theme of the research group SoC-SiP (2009-2013)

Domain

Hardware security

Keywords

Cryptography
Cryptoprocessor
Hardware and software attacks
FPGA
NoC
Code Obfuscation

Contact

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