



# Vianney LAPÔTRE

Associate Professor  
Embedded systems

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*Today's research is the foundation  
for tomorrow's technologies*

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## BIO

After obtaining his Ph.D in 2013 on reconfigurable multi-core architectures applied to telecom issues, Vianney Lapôtre returns to UBS in 2014, where he currently works on the security of embedded systems. Previously, as a Post-doctoral fellow at the LIRMN Lab (Montpellier) he participated in the European Mont-Blanc project where the technologies derived from embedded systems were studied to design tomorrow's energy efficient supercomputers. Since 2017, his research focuses on embedded processors security, particularly related to Industry 4.0 applications. Offering secure processors is a priority for him so that the greatest number benefit from trustworthy technologies.



Link to full biography



100% of the researcher's activity devoted to cybersecurity

Focus :

Research

Application field

## Core data

**PhD students:** 6

**Post-doctoral fellows:** 1

**Publications:** 10 - IEEE TVLSI, IEEE TC, ACM TECS, etc.

**Conferences:** 30 - FPL, FPT, ISCAS, ASP-DAC, ISVLSI, etc.

**International collaborations:** Ruhr-Universität Bochum (Allemagne), Information Technology University (Pakistan).

## Area(s) of research

Embedded systems security

## Fields of expertise

Protection of embedded processor architectures

Protection of embedded system architectures

## Applicative examples

Protection of connected objects

Protection of sensitive data

## Collaborative projects

HardBlare (CominLabs 2015-2019 project) with IRISA, INRIA & IETR Rennes Labs.

TSUNAMY (NRA 2013-2017 project) with the Hubert Curien (St Etienne), LIP6 (Paris) & CEA Saclay Labs.

## Domain

Hardware security

## Keywords

Hardware and software attacks

On-board processors  
FPGA

## Contact

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